





Mode A3 : Signal ON Delay (Timer doe not resets when power comes on)

Mode B : Repeat Cycle 1 (Timer resets when power comes on) Output can be sustained or one-shot.

Mode B1: Repeat Cycle 2 (Timer does not reset when power comes on) Output can be sustained or one-shot.

Mode B2: Repeat Cycle ON sta (Timer does not rese when power comes Output can be susta or one-shot.

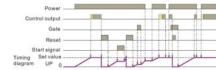
Mode C: (Timer reset when power comes on)

Mode D: Signal OFF delay (Timer resets when power comes on)

Mode E: Interval (Timer resets when power comes on)

Mode F: Cumulative (Timer does not reset when power comes ON.)

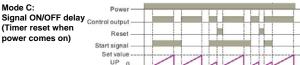




Control output			 	
Gate .				
Reset -		1	1111	
Start signal	1	12 1 1		

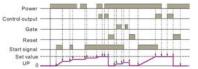
Control o	utput			-
	Gate			-
	Reset			<u> </u>
Start s		the states		1

art et on) iined	Power Control output Reset Start signal Set value UP 0	
	UP 0	









Timing starts when the start signal goes ON. The control output is controlled using a sustained or one-shot time period. The start signal disables the timing function (i.e., same function as the gate input)

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ming starts when the start signal goes ON. ne status of the control output is reversed hen time is up (OFF at start) hile the start signal is ON, the timer starts hen the power comes ON or when the eset input goes OFF.

Timing starts when the start signal goes ON. The status of the control output is reversed when time is up (OFF at start) While the start signal is ON, the timer starts when the power comes ON or when the reset input goes OFF.

Timing starts when the start signal goes ON. The status of the control output is reversed when time is up (OFF at start) While the start signal is ON, the timer starts when the power comes ON or when the reset input goes OFF.

Timing starts when the start signal goes ON or OFF.

The status of the control output is ON when the start signal goes ON or OFF.

The control output is ON when the start signal is ON (except when the power is OFF or the reset is ON).

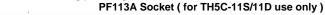
Timing starts when the start signal comes ON. The control output is reset when time is up. While the start signal is ON, the timer starts when the power comes ON or when the reset input goes OFF.

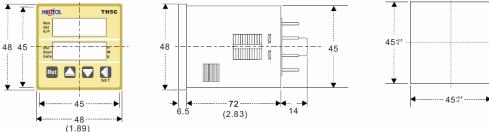
Start signal enables timing (timing is stopped when the start signal is OFF or when the power is OFF) A sustained control output is used.

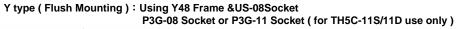


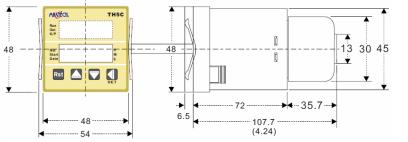
Unit : mm (inch)





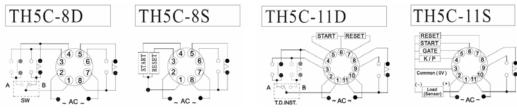






6 **Terminal Arrangement**

Confirm that the power supply meets specifications before use.



Type Selection

Туре	TH5C-8D	TH5C-8S	TH5C-11D	TH5C-11S		
Time range	9.999s/99.99s/999.9s/9999s/999.9m/9999m/999.9h/9999h/99m59s/99h59					
Output contact	2C or 1A1C	1C	2C or 1A1C	1C		
Memory		Ô	0	0		
External Reset		0	0	0		
External Start		\odot	\odot	\odot		
External Gate				\odot		
Key protect (K/P)				\odot		